Question 1. 4-bit Assembly: Consider a theoretical 4-bit CPU shown in 1 with the instruction set shown in Table 1. This figure is taken from the Crash Course Computer Science series episodes 7 and 8. These videos are good resources for this question (although there are a few small differences in the implementation between Table 1 and the instructions in the videos. CPU4sim.m is a MATLAB tool for simulating the CPU. CPU4sim allows you to program the CPU using a simple assembly programming language described in Table 1. In this language the code:

LDA 14
LDB 15
ADD A B
STA 13
HLT

tells the CPU to add the value stored in memory location 14 to the one in location 15 and store the result in memory location 13. To input this program into CPU4sim you define a variable called prog. Here is the pre-programmed example from the first few lines of CPU4sim.m:

```matlab
function CPU4sim(prog)
  cf;

  if(~exist('prog','var') || isempty(prog))
    [prog{1:16}]=deal(''); % set all 16 locations to empty
  endif

  % code
  prog{1}='LDA 14';
  prog{2}='LDB 15';
  prog{3}='ADD A B';
  prog{4}='STA 13';
  prog{5}='HLT';

  % data
  prog{15}=45;
  prog{16}=13;

end
```

The variable prog is a cell array so it can store different types of data. If the type of data is a string of characters like the first element prog{1}='LDA 14'; then the string is converted to an 8-bit instruction using Table 1. If the entry is a number like prog{16}=13; then the number is stored directly into memory. The index n in prog{n} determines which memory location. So prog{7}='SUB C D'; would store the instruction for SUB C D in the 7th memory location. The CPU has 16 memory locations addressed from
0–15 so the 7th location is $ADDR = 6$. To program the simulator update the %% Program section with new instructions and run the code. When the simulator starts all of the registers are random so a RESET is required before beginning the FETCH/DECODE/EXECUTE cycle. In class we wrote a program to find the remainder when the number in $ADDR = 14$ is divided by the number in $ADDR = 15$. The result is stored in $ADDR = 13$. Here is the program:

00:LDA 14
01:LDB 15
02:SUB A B
03:JPN 06
04:STA 13
05:JMP 02
06:HLT;

The line numbers are there to help with the jump commands but are not allowed when programming CPU4sim. Also the spaces are important in CPU4sim. So SUB A B is not the same as SUBAB. To program CPU4sim to calculate the remainder use the following:

```matlab
%% Program
if(~exist('prog','var') | isempty(prog))
    prog{1:16}=deal('');
end

% code
prog{1}='LDA 14';
prog{2}='LDB 15';
prog{3}='SUB A B';
prog{4}='JPN 06'; %JPN 7th
prog{5}='STA 13';
prog{6}='JMP 02'; %JMP 3rd
prog{7}='HLT';

% data
prog{15}=45;
prog{16}=13;
end
```

The comments like %JPN 7th are there to make it clear to "jump if negative" to the 7th memory location which is $ADDR = 06$.

(1) Write a program in this assembly language to find the maximum of two numbers stored in $ADDR = 15$ and $ADDR = 14$ and store the result in $ADDR = 13$. You can test it in CPU4sim. If CPU4sim gets stuck in a loop, and the reset button will not stop it, then you can press ctrl-c in the command window to stop it. If the font is too big or small you can use this command `set(findall(gcf,'property','FontSize'),'FontSize',10)` to change it. 8 is the default size. If you want to change the data in RAM while CPU4sim is running you can delete everything in the location and type a single number and press return.

(2) Write a program to divide the number in $ADDR = 14$ by the number in $ADDR = 15$ and store the result in $ADDR = 12$ and the remainder in $ADDR = 13$.

(3) Write a program to multiply two numbers who product is less than or equal to 255. So $17 \times 12 = 204$ is okay, but $16 \times 16 = 256$ is not. Be sure to think about edge cases like $0 \times 0 = 0$.  

<table>
<thead>
<tr>
<th>4-bit opcode</th>
<th>Name</th>
<th>Address or Registers</th>
<th>Example</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0000 (0)</td>
<td>NOP</td>
<td>none</td>
<td>![NOP]</td>
<td>No operation but (increment IP)</td>
</tr>
<tr>
<td>0001 (1)</td>
<td>LDA</td>
<td>4-bit ADDR</td>
<td>![LDA]</td>
<td>Load contents of ADDR into register A</td>
</tr>
<tr>
<td>0010 (2)</td>
<td>LDA</td>
<td>4-bit ADDR</td>
<td>![LDB]</td>
<td>Load contents of ADDR into register B</td>
</tr>
<tr>
<td>0011 (3)</td>
<td>LDA</td>
<td>4-bit ADDR</td>
<td>![LDC]</td>
<td>Load contents of ADDR into register C</td>
</tr>
<tr>
<td>0100 (4)</td>
<td>LDA</td>
<td>4-bit ADDR</td>
<td>![LDD]</td>
<td>Load contents of ADDR into register D</td>
</tr>
<tr>
<td>0101 (5)</td>
<td>STA</td>
<td>4-bit ADDR</td>
<td>![STA]</td>
<td>Store contents of register A into ADDR</td>
</tr>
<tr>
<td>0110 (6)</td>
<td>STA</td>
<td>4-bit ADDR</td>
<td>![STB]</td>
<td>Store contents of register B into ADDR</td>
</tr>
<tr>
<td>0111 (7)</td>
<td>STA</td>
<td>4-bit ADDR</td>
<td>![STC]</td>
<td>Store contents of register C into ADDR</td>
</tr>
<tr>
<td>1000 (8)</td>
<td>STA</td>
<td>4-bit ADDR</td>
<td>![STD]</td>
<td>Store contents of register D into ADDR</td>
</tr>
<tr>
<td>1001 (9)</td>
<td>JMP</td>
<td>4-bit ADDR</td>
<td>![JMP]</td>
<td>Jump to 4-bit memory address ADDR</td>
</tr>
<tr>
<td>1010 (10)</td>
<td>JPO</td>
<td>4-bit ADDR</td>
<td>![JP0]</td>
<td>Jump to ADDR if O-flag (Overflow) is set</td>
</tr>
<tr>
<td>1011 (11)</td>
<td>JPN</td>
<td>4-bit ADDR</td>
<td>![JPN]</td>
<td>Jump to ADDR if N-flag (Negative) is set</td>
</tr>
<tr>
<td>1100 (12)</td>
<td>JPZ</td>
<td>4-bit ADDR</td>
<td>![JPZ]</td>
<td>Jump to ADDR if Z-flag (Zero) is set</td>
</tr>
<tr>
<td>1101 (13)</td>
<td>ADD</td>
<td>R1 R2</td>
<td>![ADD]</td>
<td>R1=R1+R2 e.g., store A+C in A (Flags:OZ)</td>
</tr>
<tr>
<td>1110 (14)</td>
<td>SUB</td>
<td>R1 R2</td>
<td>![SUB]</td>
<td>R1=R1-R2 e.g., store B-C in B (Flags:NZ)</td>
</tr>
<tr>
<td>1111 (15)</td>
<td>HLT</td>
<td>none</td>
<td>![HLT]</td>
<td>Halt operation and (decrement IP)</td>
</tr>
</tbody>
</table>

Table 1. Instruction set for a theoretical 4-bit CPU. ADDR represents one of the 16 4-bit memory addresses. For example, the full 8-bit instruction to store register A in memory location 12 (LDA 12) is 0b01101100. \( Rn \) where \( n \) is 1 or 2 represent 2-bit addresses of registers A=0b00, B=0b01, C=0b10, D=0b11. For example the full 8-bit code for (ADD D A) is 0b11011100.
Figure 1. Layout for a theoretical 4-bit CPU.

Question 2. Introduction to MATLAB: Go to Introduction to MATLAB and download the MATLAB Primer. Go through the steps and write up 3 out of the 7 problems at the end. If you are already a MATLAB expert you could do parts or all of the Advanced (Particle Tracking) exercises here.